

## Proposed Functionality on Phased Arrays

J. W. D. Jackson  
J. C. L. Laboratories  
1000 3rd Avenue  
Seattle, WA 98101

### Abstract

Current descriptions of phased array systems tend to emphasize subcomponents limitations, including their limitations, rather than functional requirements. Functional descriptions of architecture components make it easier to break up design problems into smaller pieces that can be handled separately by realistic system designers, and hardware developers in geographically and financially separate institutions. This is true because changing to a different subcomponent may also change the nature of the limitations.

For example, using a Mach-Zehnder modulator and a mode locker can each be used to modulate a R signal at an optical carrier, yet each has its own set of limitations in terms of size, speed, bandwidth, and power handling requirements. In some application scenarios, these subcomponents can be easily exchanged. In others, requirements may force one towards the use of a specific component. Although the subcomponent details are important in the final stages of design, it is more important for the systems overview, to examine the architecture in functional terms.

This paper proposes a common set of symbols for laying out phased array architectures. The proposed descriptors provide information on the size of the array, the sequence and methods of tanning out the array, and the placement of the onto electronic interface within the overall architecture. Examples will be drawn from the author's review paper "A Survey of Optical Antennas" [1].